

# 3.5W Ultra Low-EMI Anti-Clipping Mono Class D Audio Power Amplifier

# FEATURE

 Patented technology (EDMA) to reduce the internal Rdson and heat loss, increase the output power

Po=3.5 W (V<sub>DD</sub>=5.0V, R<sub>L</sub>=4Ω, THD+N=10%)

Po=3.0 W (V<sub>DD</sub>=3.7V, R<sub>L</sub>=2Ω, THD+N=10%)

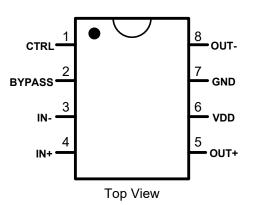
- Excellent EMI Suppression Performance
  - a) Using Patented technology (AROC, Active edge Ringing and Overshoot Control circuitry)
  - b) The emission level is highly lower than FCC Part15 Class B standard;
  - c) Without interference to FM radio, CMMB, GSM, CDMA, Bluetooth and any other sensitive modules with different bands;
- d) The difficulty of system design is reduced
- THD+N=0.05% (R<sub>L</sub>=4Ω, P<sub>O</sub>=1W, f=1kHz)
- Low noise, High SNR
- Anti-Clipping Function (ACF)
- Excellent Click-Pop Noise reduction function
- Low Shutdown Current, 0.01µA
- Filter-less Modulation, Eliminating Output Filter
- Over Current Protection, Thermal Protection, Low voltage malfunction prevention function included
- Pb-Free Packages, SOP8L, SOP8L-PP, DFN8L(5mm\*6mm)

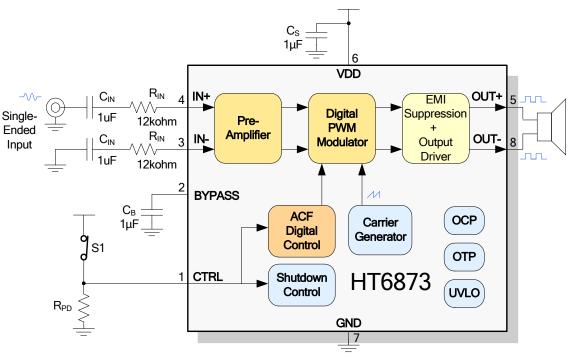
# ■ TYPICAL APPLICATION

# APPLICATIONS

- Bluetooth Speakers
- 2.1 Channel Speakers
- iPhone/iPad docking
- Tablet PC/Note Book
- LCD TV/Monitor
- Portable Speakers
- Megaphone
- MP4/GPS
- Smart phones
- Portable Gamers

#### TERMINAL CONFIGURATION







# ■ TERMINAL FUNCTION \*1

SOP8L/DFN8L Terminal No.	Name	I/O	ESD composition	Function
1	CTRL	А	PN	Shutdown and ACF control terminal
2	BYPASS	А	PN	Analog reference terminal
3	IN-	А	PN	Negative input terminal (differential -)
4	IN+	А	PN	Positive input terminal (differential +)
5	OUT+	0	-	Positive output terminal (differential +)
6	VDD	Power	-	Power supply
7	GND	GND	-	GND
8	OUT-	0	-	Negative output terminal (differential -)

\*1: Input terminal

Input terminal O: Output terminal A: Analog terminal when a voltage that is higher than the VDD potential is impressed into the terminal of PN (ESD protection circuit is composed of PMOS and NMOS), the leakage current flows through the protection circuit of PMOS.



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### 嘉兴禾润电子科技有限公司

#### Jiaxing Heroic Electronic Technology Co., Ltd.

地址:	浙江省嘉兴市凌公塘路3339号JRC大厦A座三层				
Add:	A 3rd floor, JRC Building, No. 3339, LingGongTang Road, Jiaxing, Zhejiang Province				
销售/Sales:	0573-82585539				
支持/Support:	0573-82586151				
传真/Fax:	0573-82585078				
E-mail:	sales@heroic.com.cn				
网址/Website:	www.heroic.com.cn				